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APPLICANTS

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** CONTINUING DATA ***** *Ch*

This application is a CON of PCT/EP02/05651 05/23/2002

** FOREIGN APPLICATIONS ***** *Ch*

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Calomela</i> Examiner's Signature Initials	GERMANY	3	24	3

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TITLE

DRAM cell arrangement with vertical MOS transistors, and method for its fabrication

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